

FIG.1

TABLE 1

Link	L1	L2	L3	L4	L5	L6	L7	N1	N2	N3	N4	N5	N6	Max
L1			1	1		1						1		1
L2			1	1		1						1		1
L3	1	1		1				1						1
L4	1	1			1		1	1		1				1
L5			1			1						1		1
L6				1	2		1		1	1				2
L7			1	1	1	1			1			1		1

FIG. 2

TABLE 2

Link	L1	L2	L3	L4	L5	L6	L7	N1	N2	N3	N4	N5	N6	Max
L1			2	2		1						2		2
L2			2	2		1						2		2
L3	1	1		1				1						1
L4	1	1			1		1	1		1				1
L5			1			1						1		1
L6				1	2		1		1	1				2
L7			1	1	1	1			1			1		1

FIG. 3

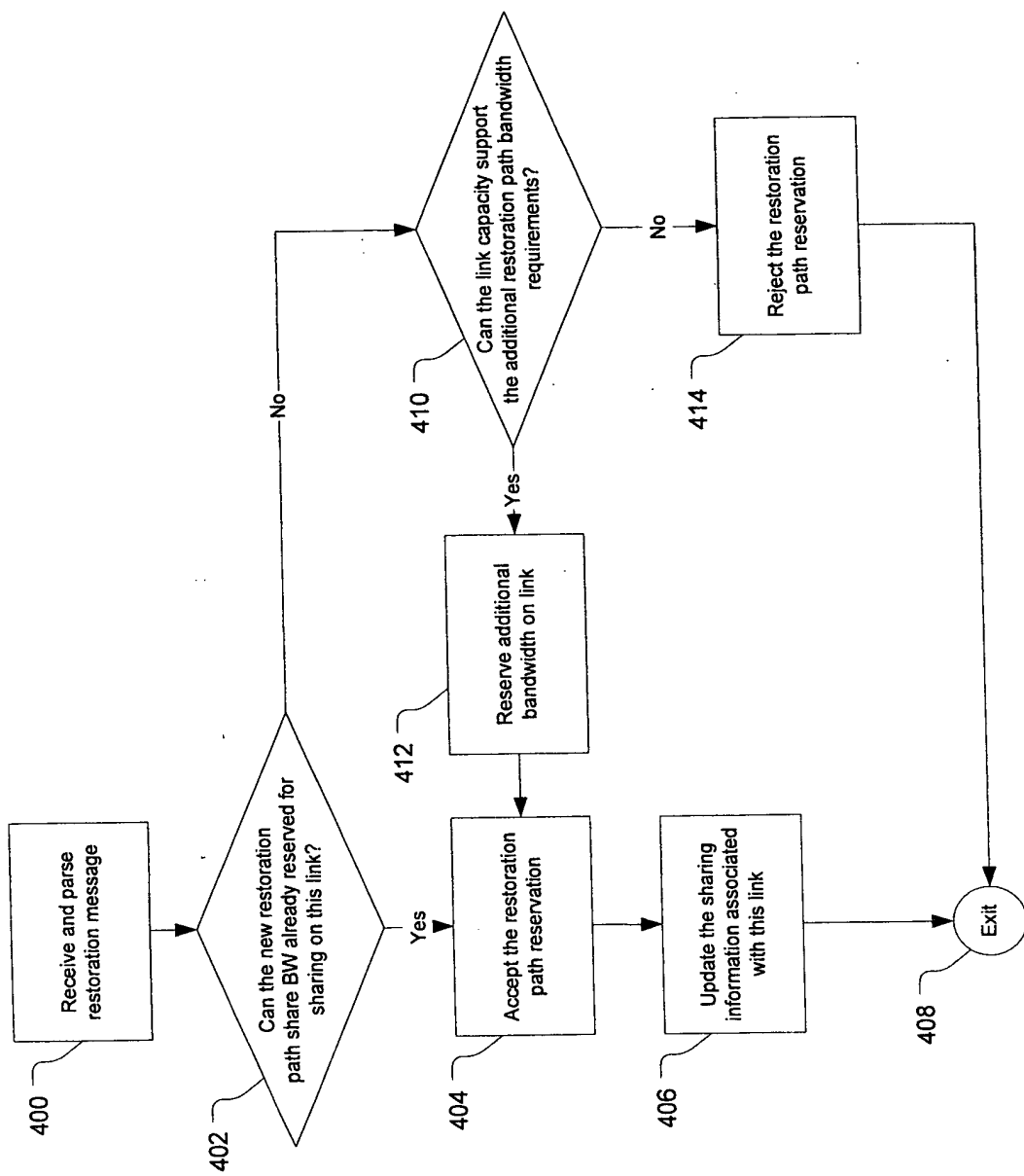


FIG. 4

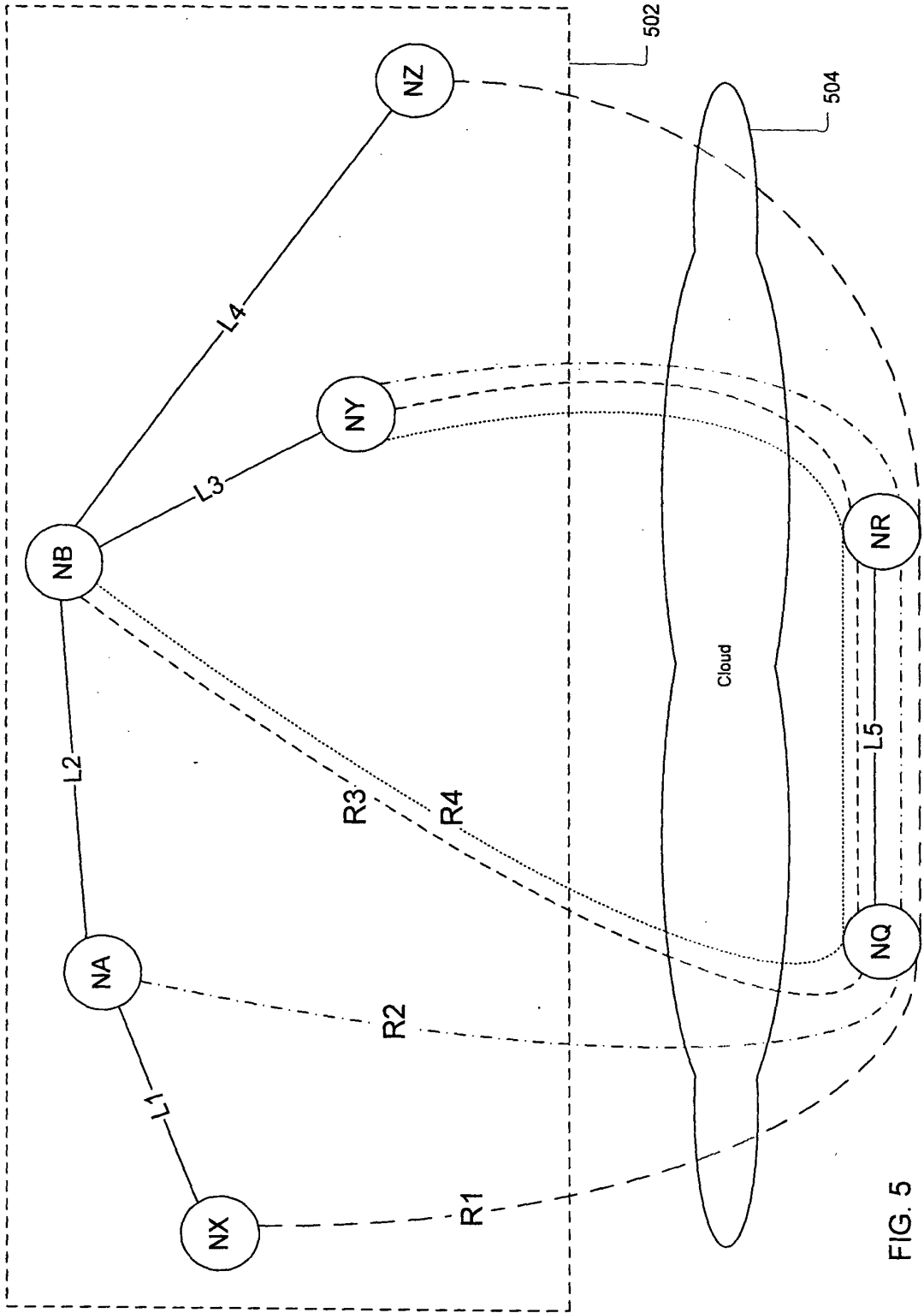


FIG. 5

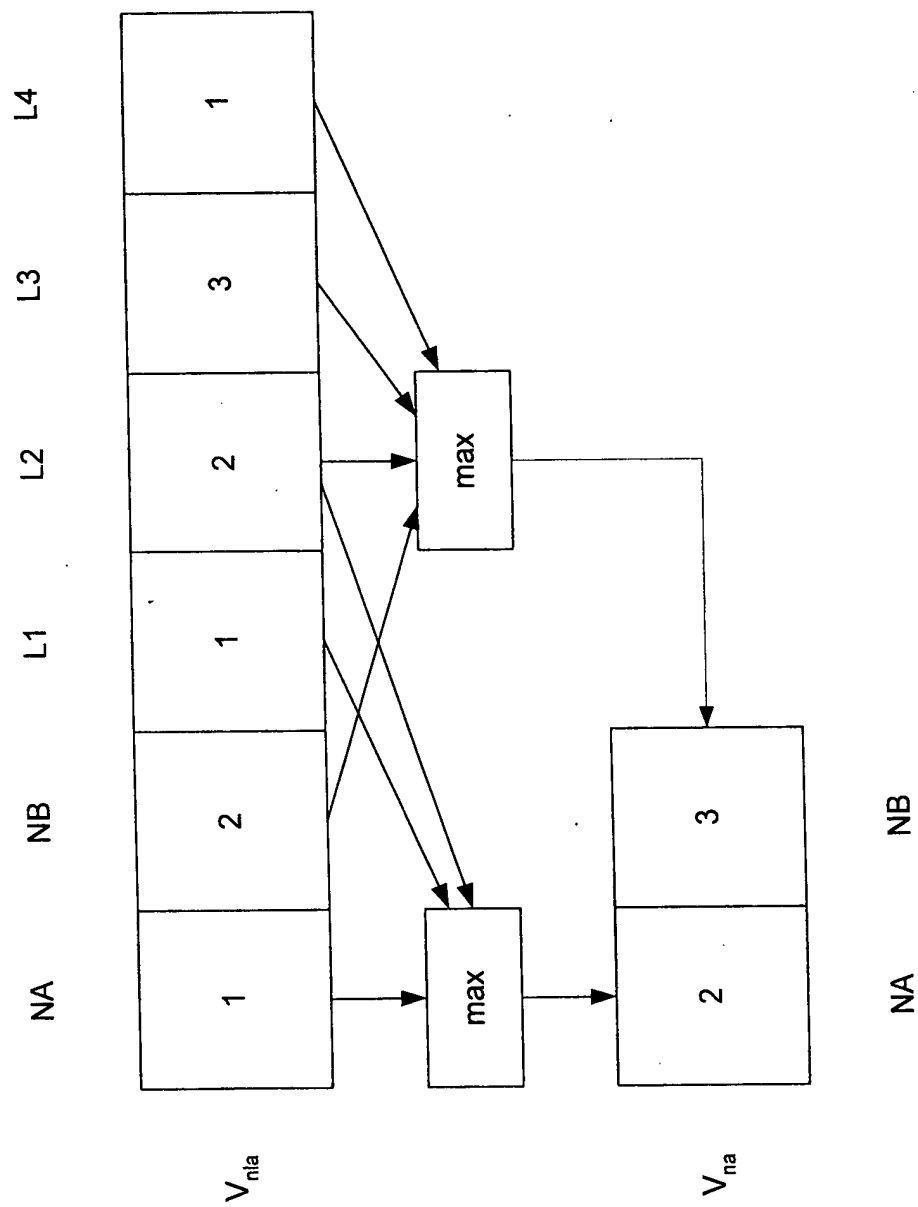


FIG. 6

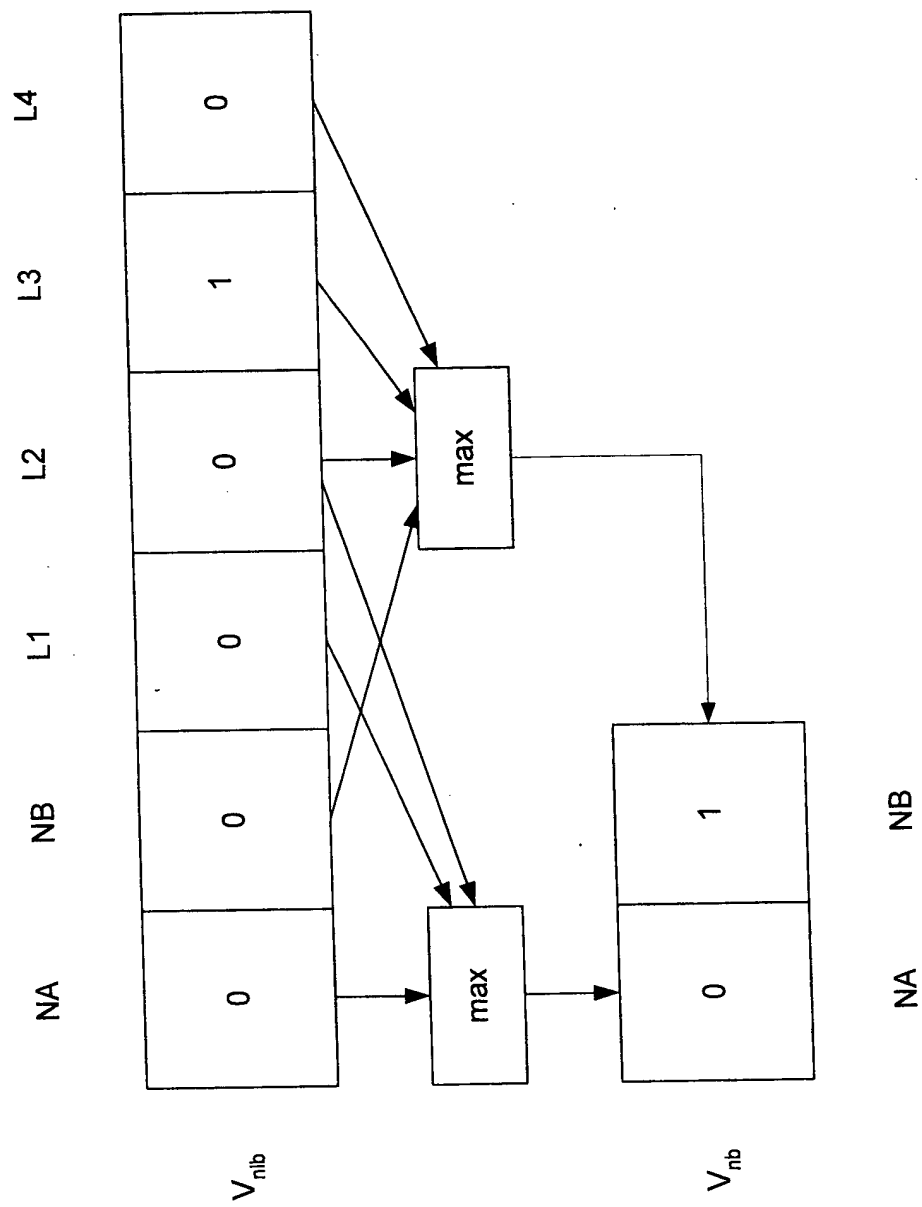


FIG. 7

500

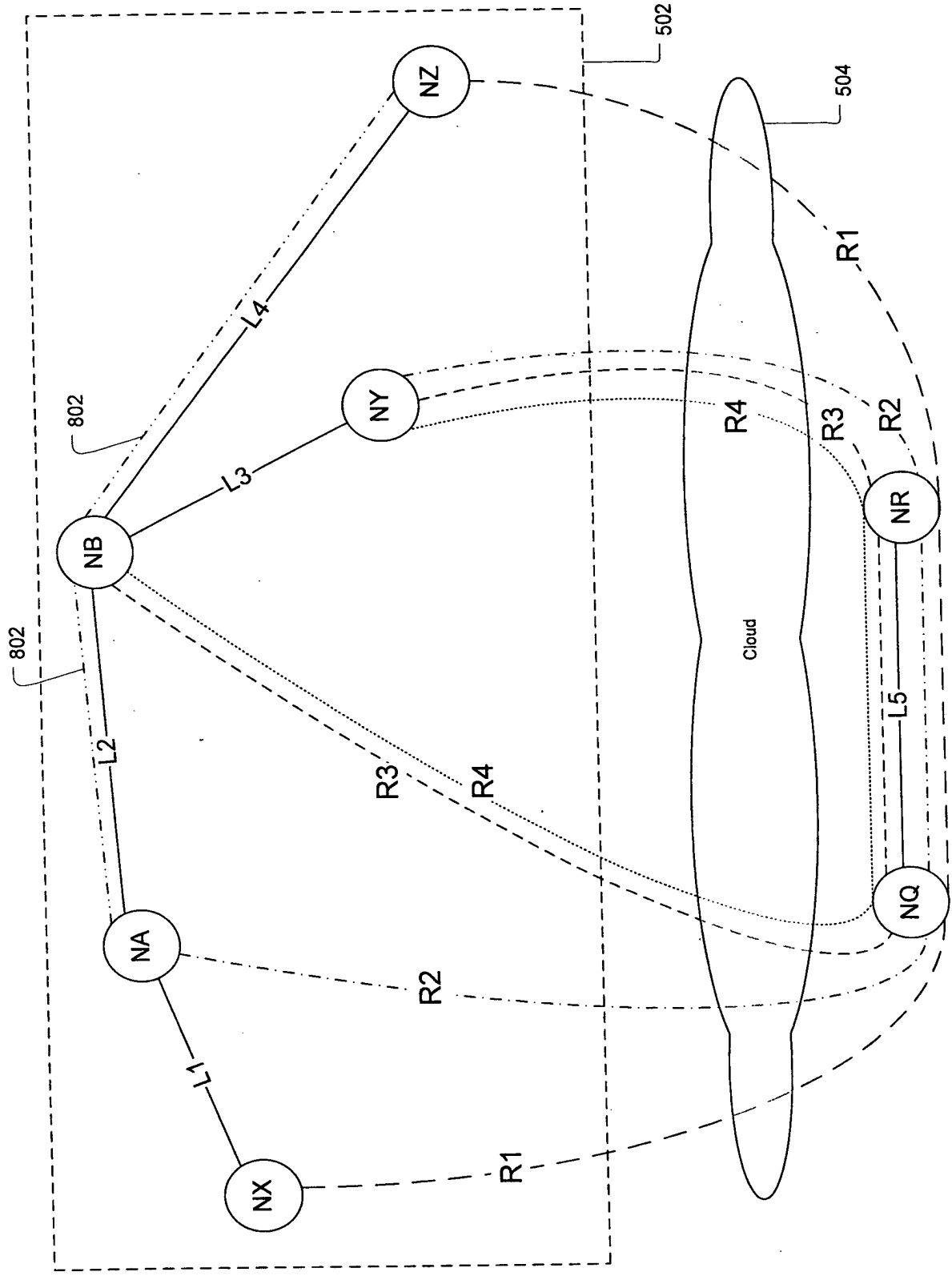


FIG. 8

	NA	NB	L1	L2	L3	L4
V_{pnl}	0	1	0	1	0	1

	NA	NB
V_{pn}	1	1

FIG. 9

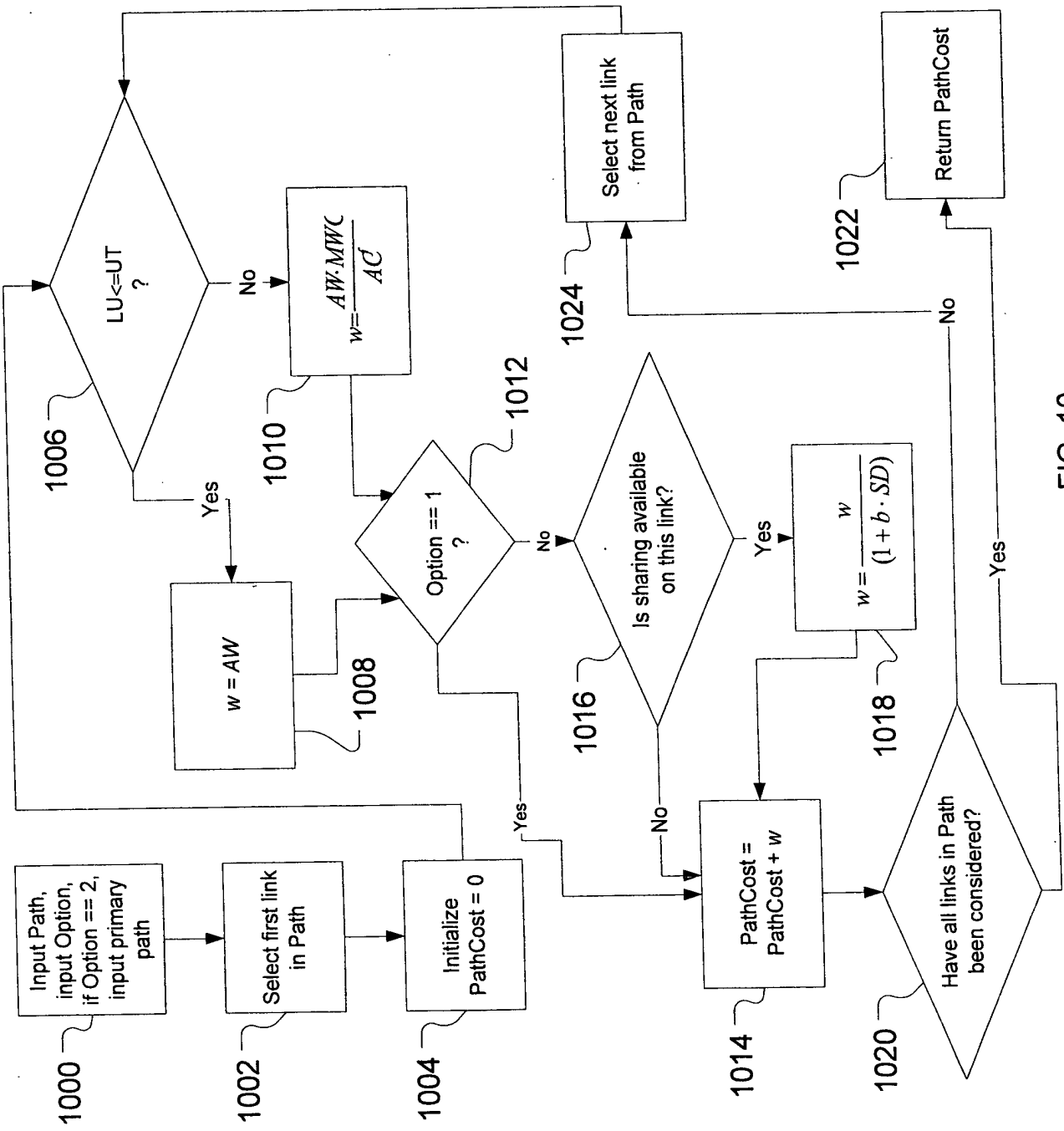


FIG. 10

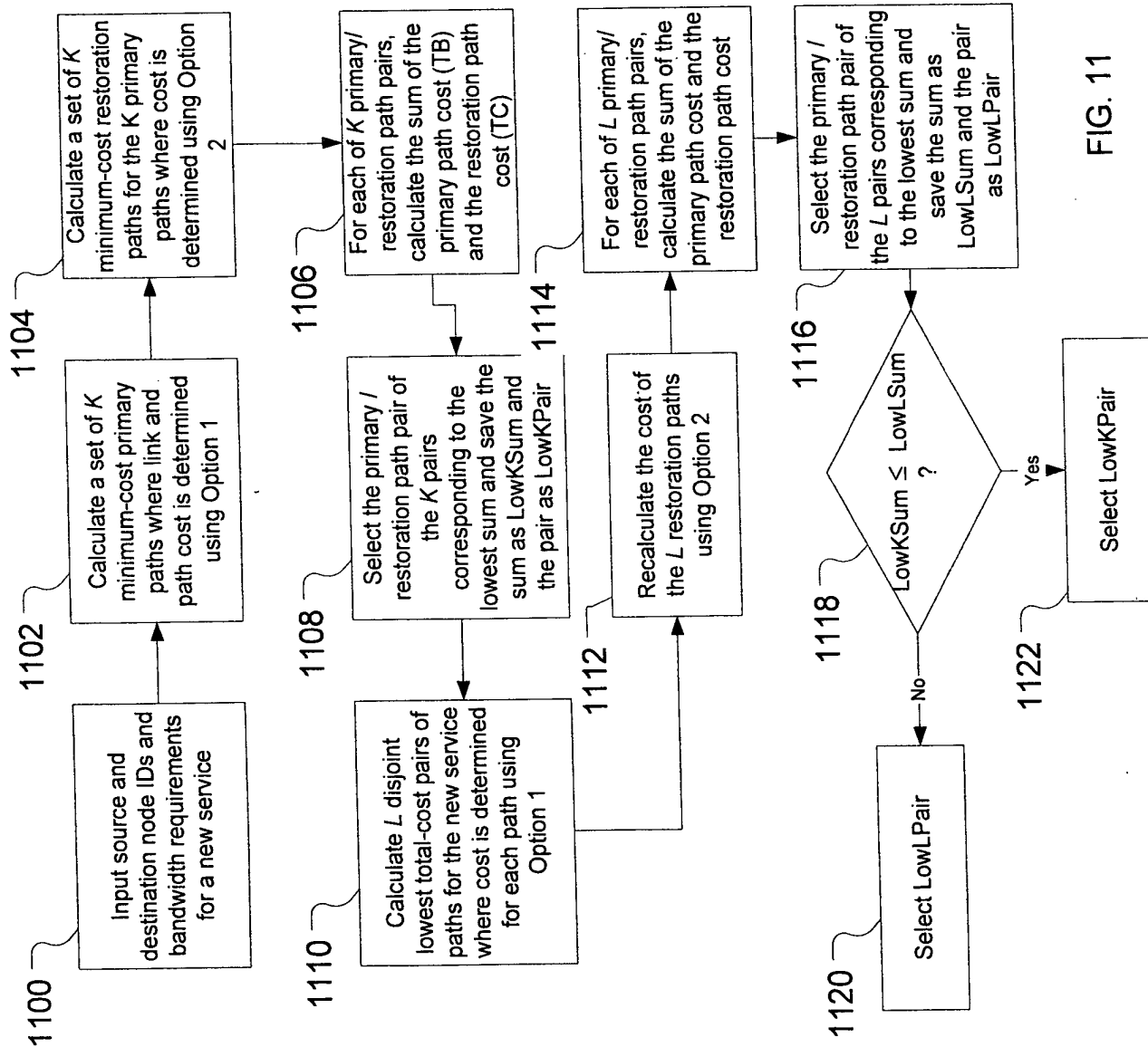


FIG. 11

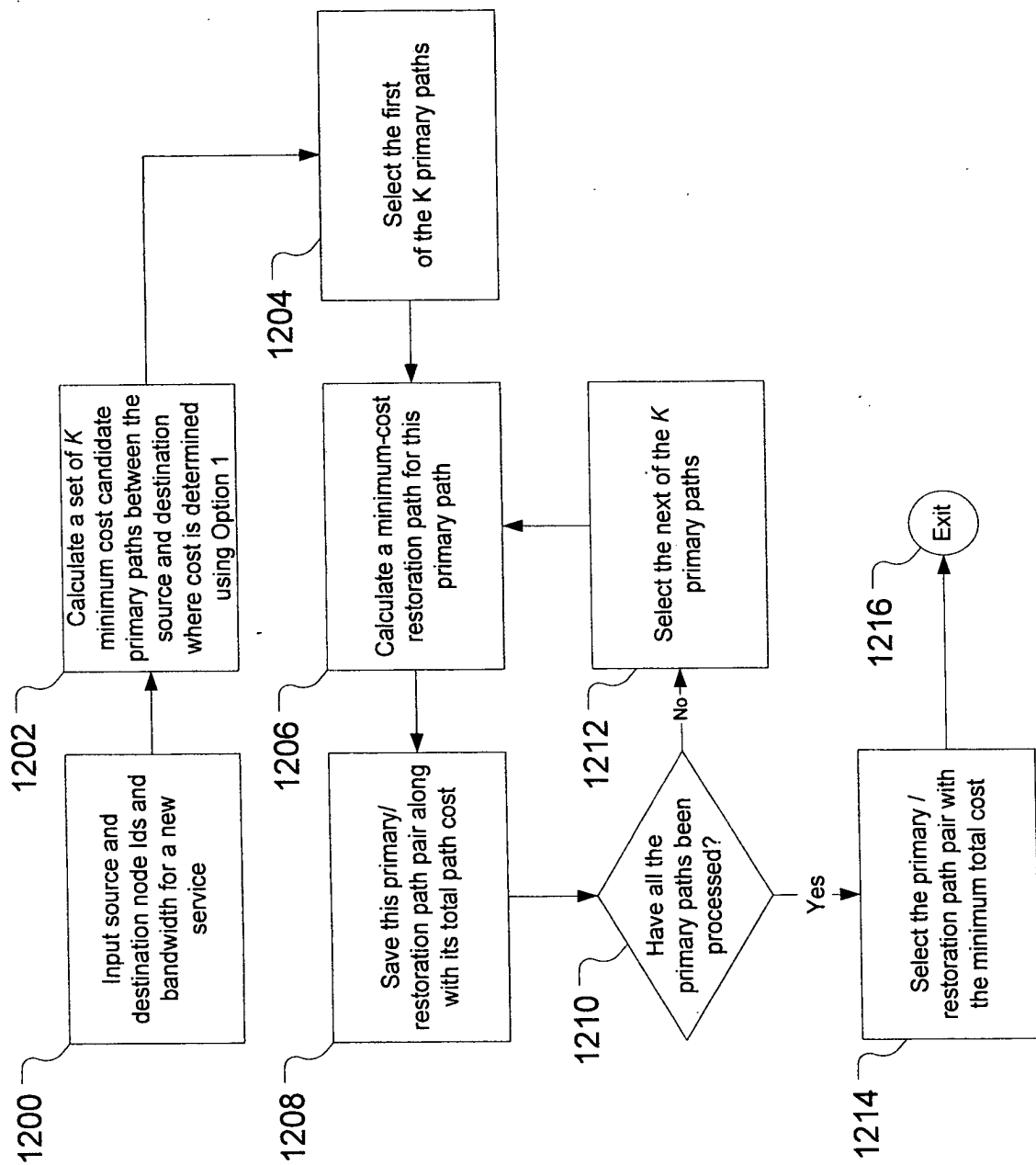


FIG. 12

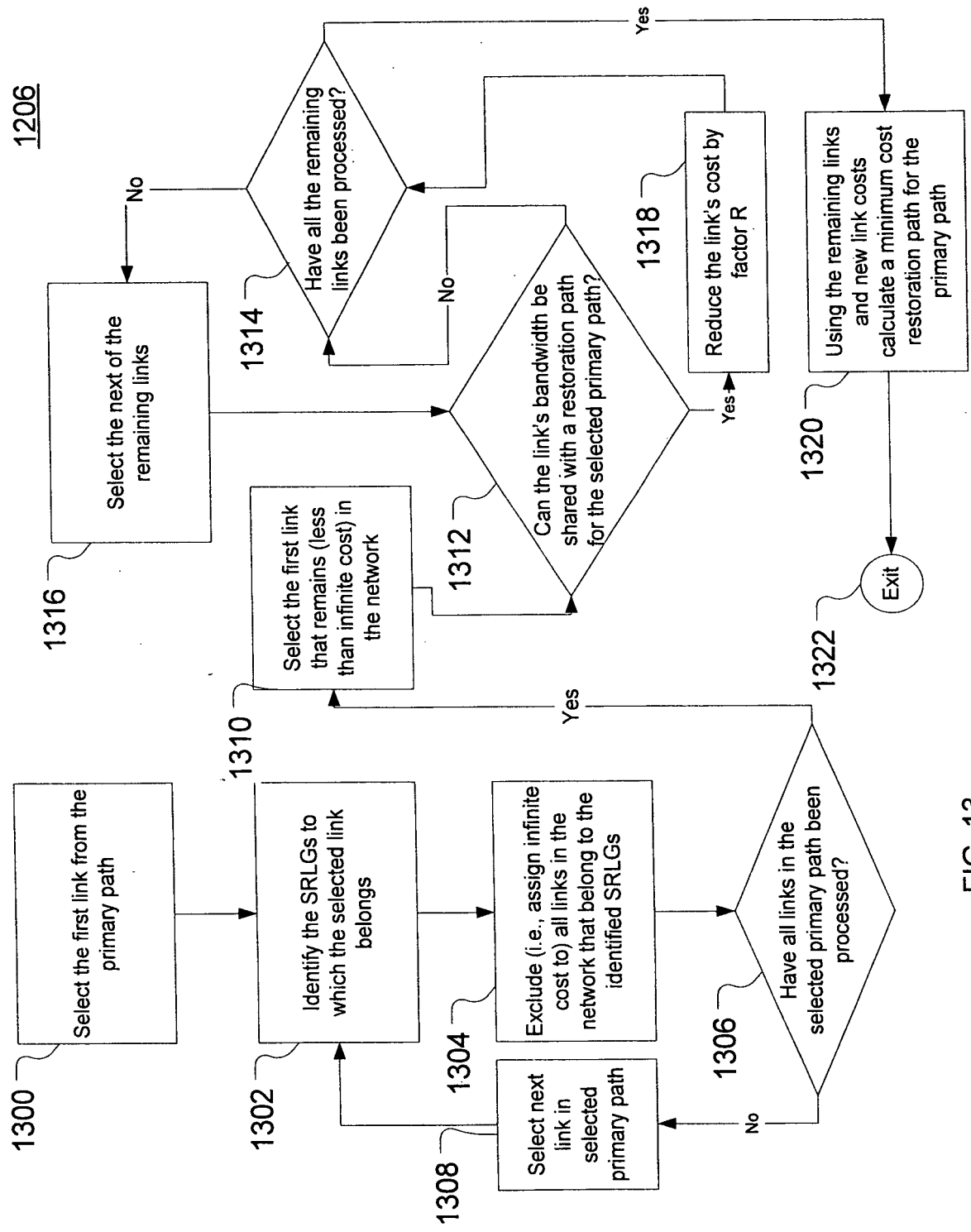


FIG. 13

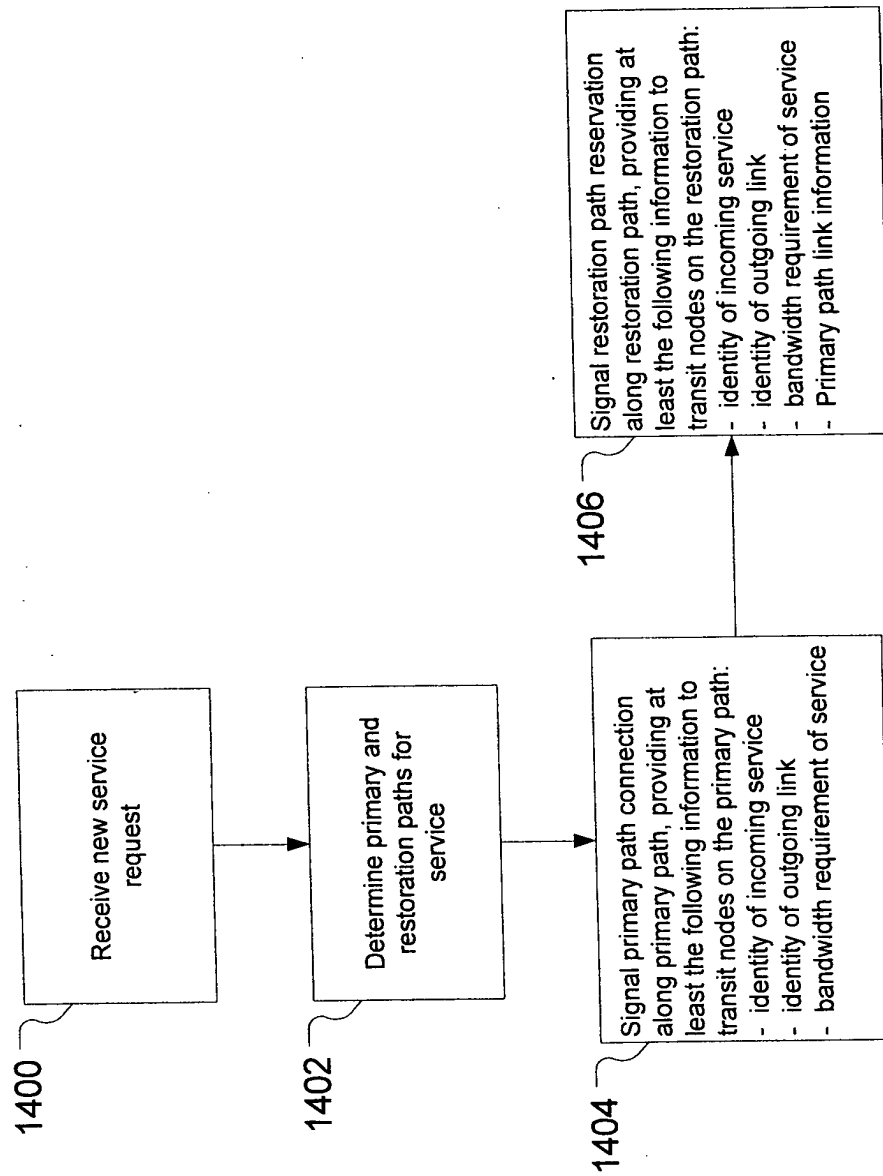


FIG. 14

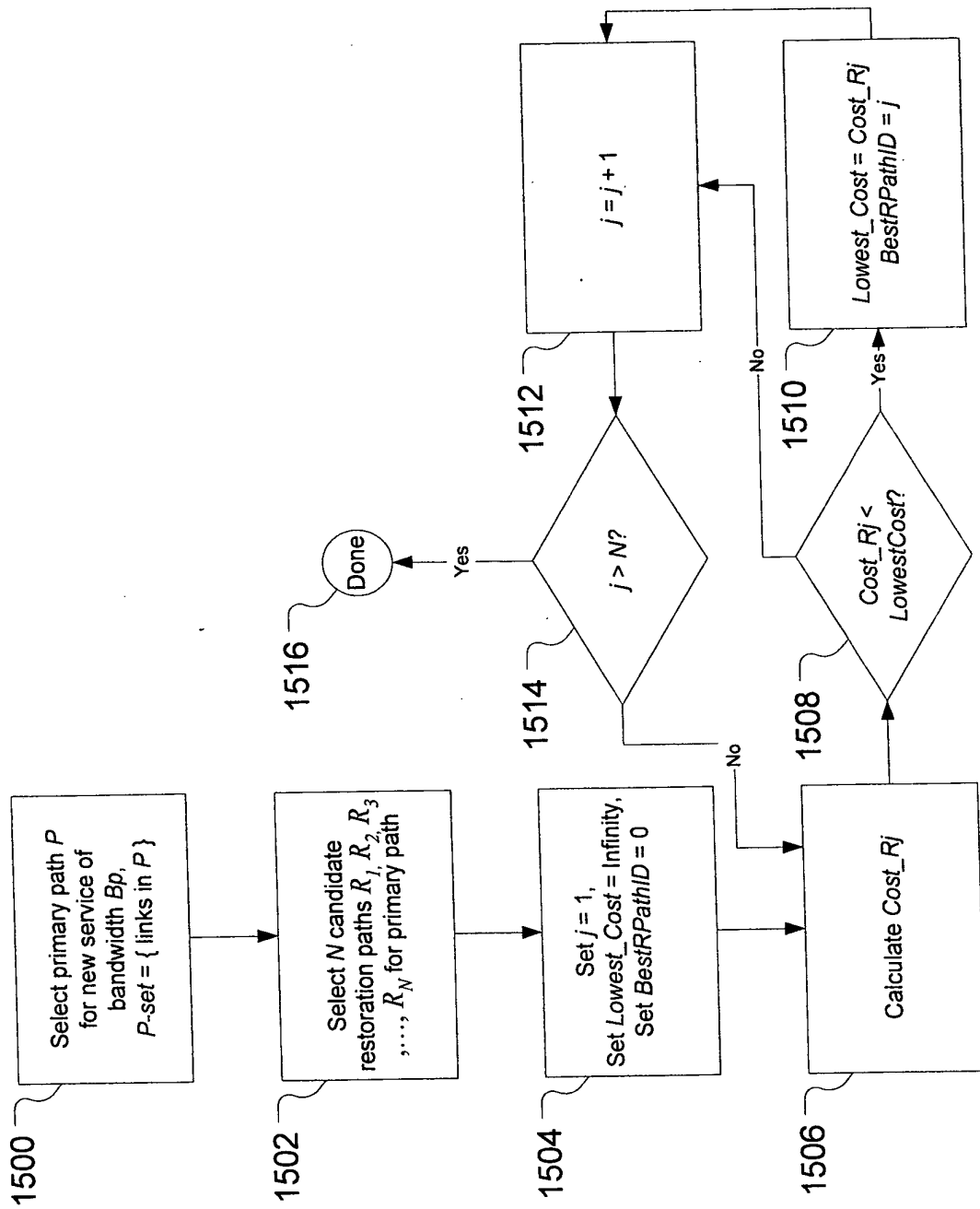


FIG. 15

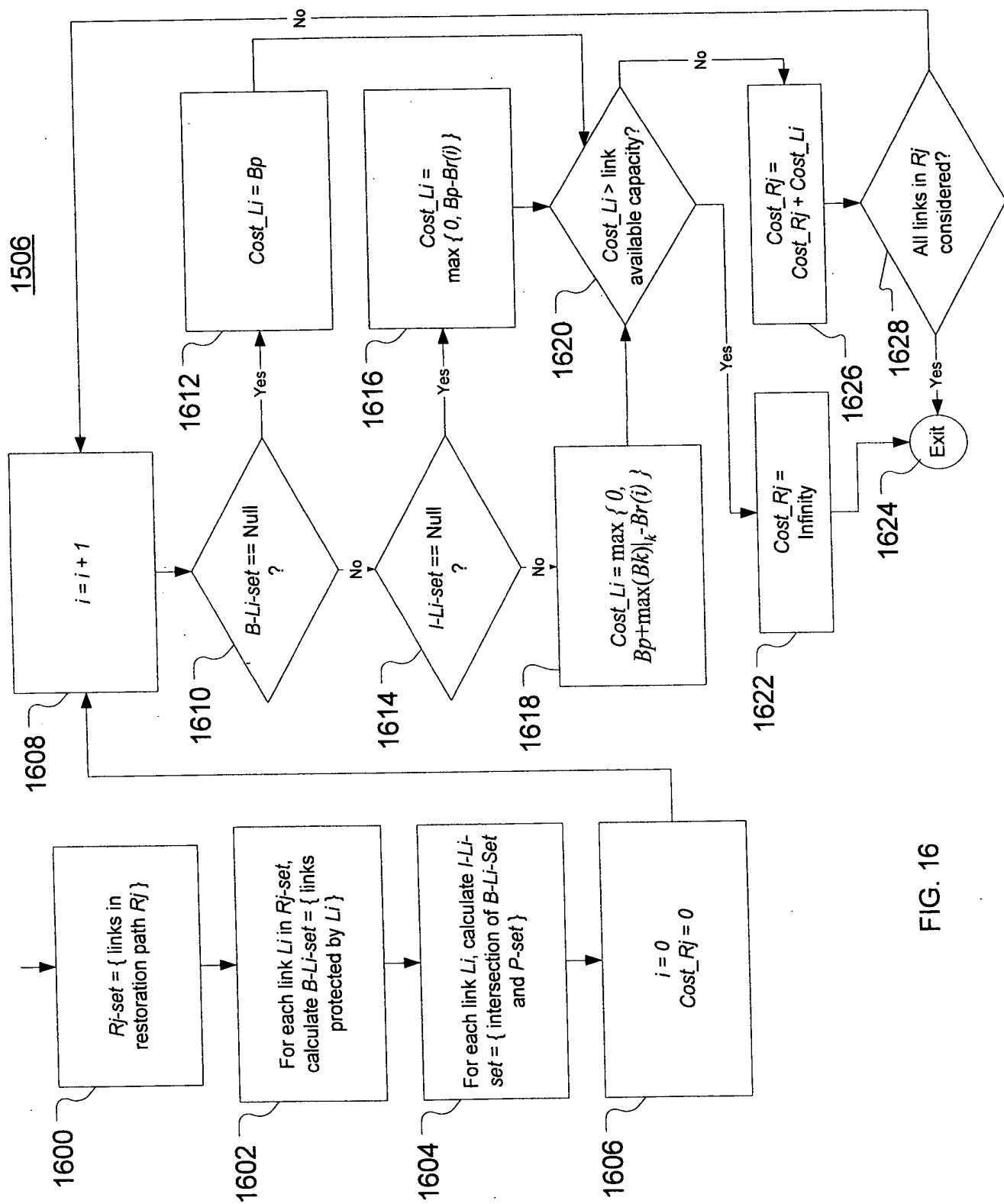


FIG. 16

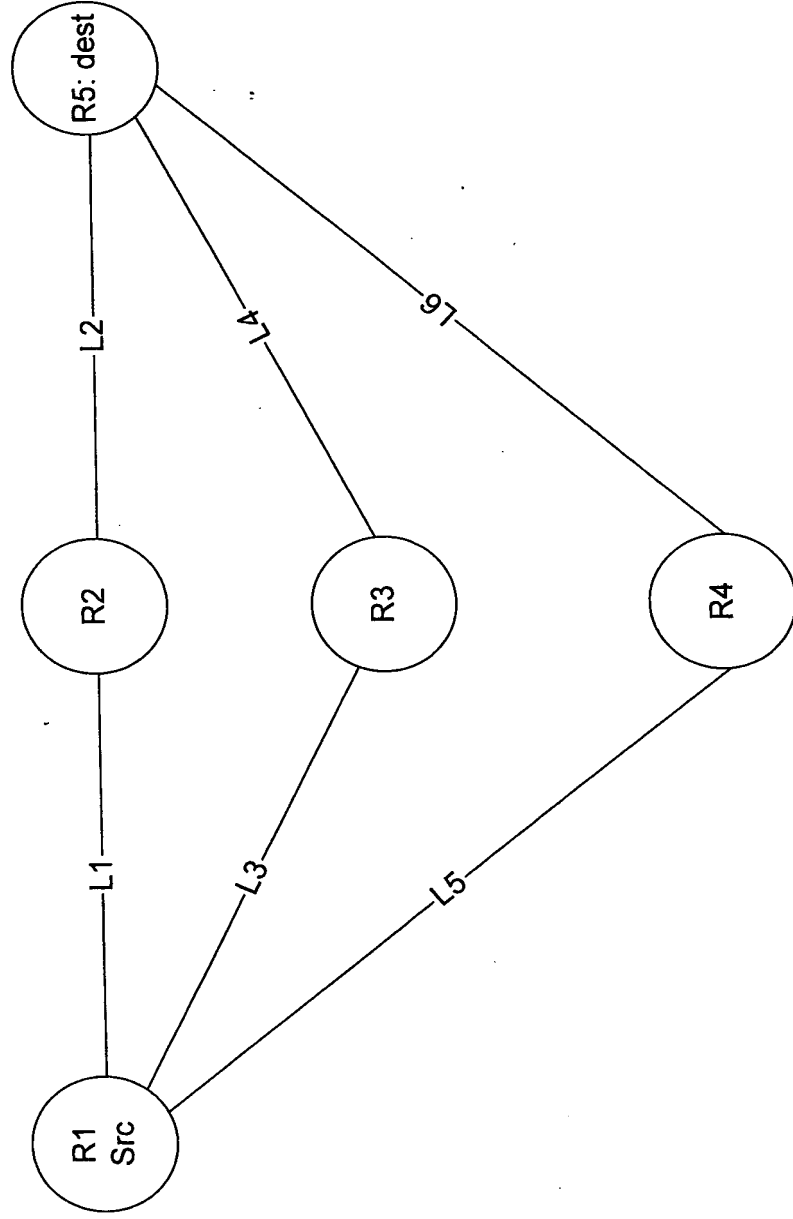
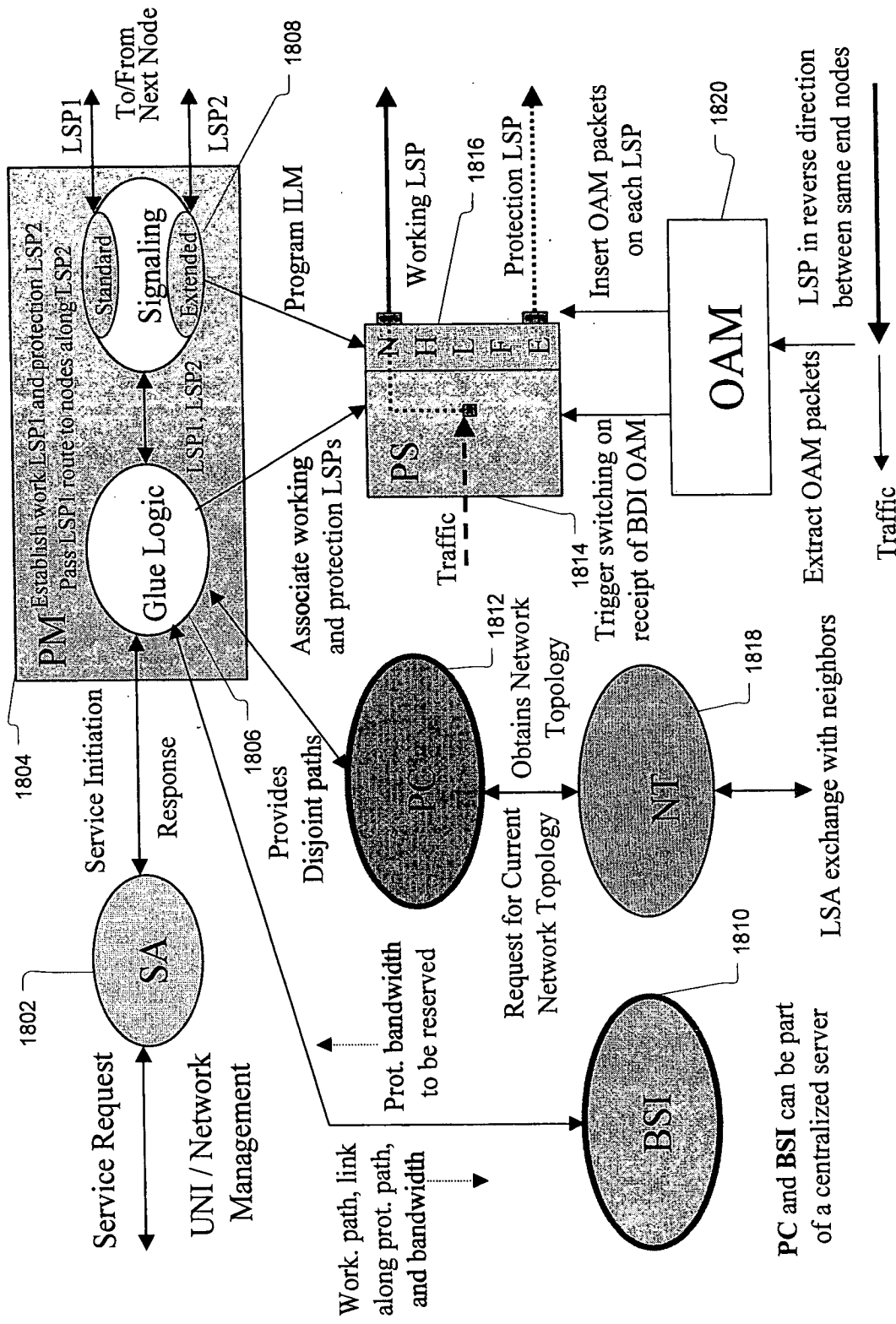


FIG. 17



Ingress Node

FIG. 18

Intermediate Nodes

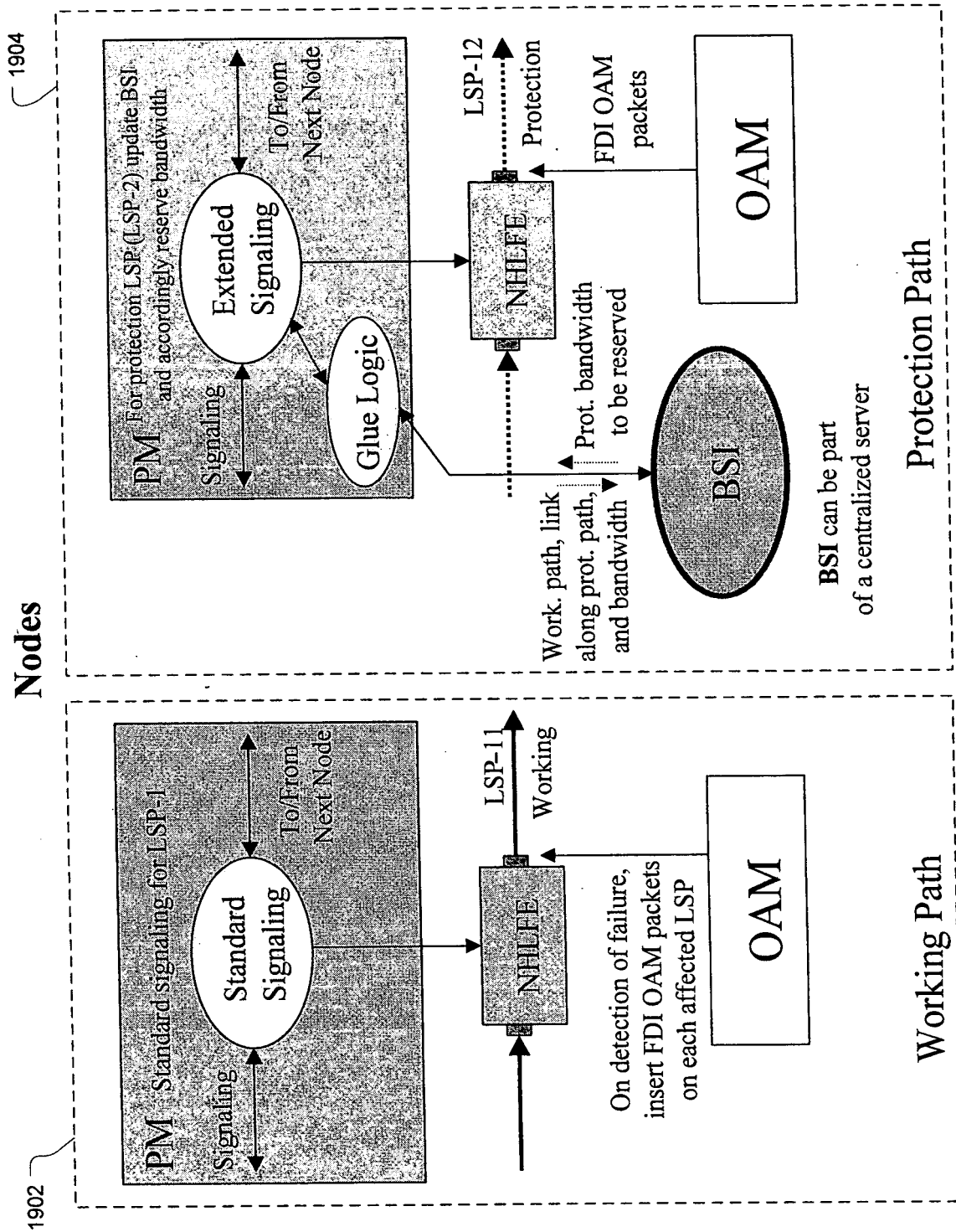


FIG.19

Egress Node

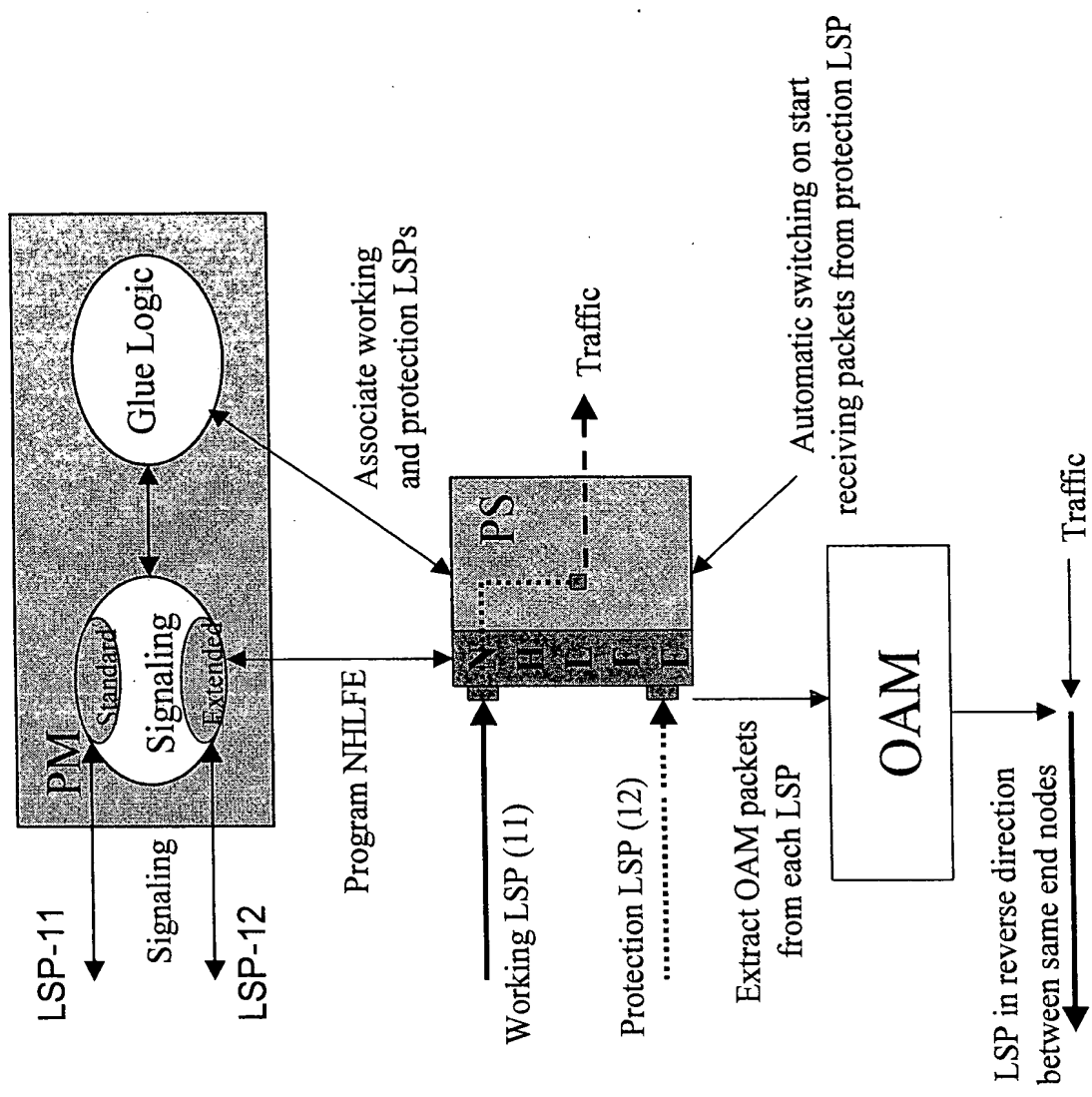


FIG. 20